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(only for nonprovisional applications under 37 CFR 1.53(b))

Inventor (s): Yasufumi UCHIDA and Yoshihiro SAEKI

TITLE:	REARRANGEMENT SHEET, SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THEREOF
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See MPEP chapter 600 concerning utility patent application contents.

ADDRESS TO: **Commissioner for Patents**
Box Patent Application
Washington, DC 20231

1. ☒ Patent Application Fee Determination Record
(Submit an original, and a duplicate for fee processing)

- 2.
- ☒
- Specification [Total Pages 41]

- (preferred arrangement set forth below)
- Descriptive title of the Invention
- Cross References to Related Applications
- Background of the Invention
- Brief Summary of the Invention
- Brief Description of the Drawings (if filed)
- Detailed Description
- Claim(s)
- Abstract of the Disclosure

3. ☒ Drawing(s) Figures 1-11C [Total Sheets 11]

- 4. Oath or Declaration** [Total Sheets 3]

- ☒
- Newly executed (original or copy)

ACCOMPANYING APPLICATION PARTS

8. ☒ Assignment & Recordation Cover Sheet [Total pages 4]

9. ☐ Information Disclosure Statement (IDS)/PTO-1449
[Total Pages] [Total References]

- 12.
- ☐
- Preliminary Amendment [Total Pages]

13. ☒ Return Receipt Postcard (MPEP 503)

- #### 14. ☐ Small Entity Assertion

15. ☒ Certified Copy of Priority Document(s)

The rights of priority are claimed under 35 U.S.C. § 119 of Japanese Application No. 2001-060348 Filed March 5, 2001

16. ☐ Other:

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